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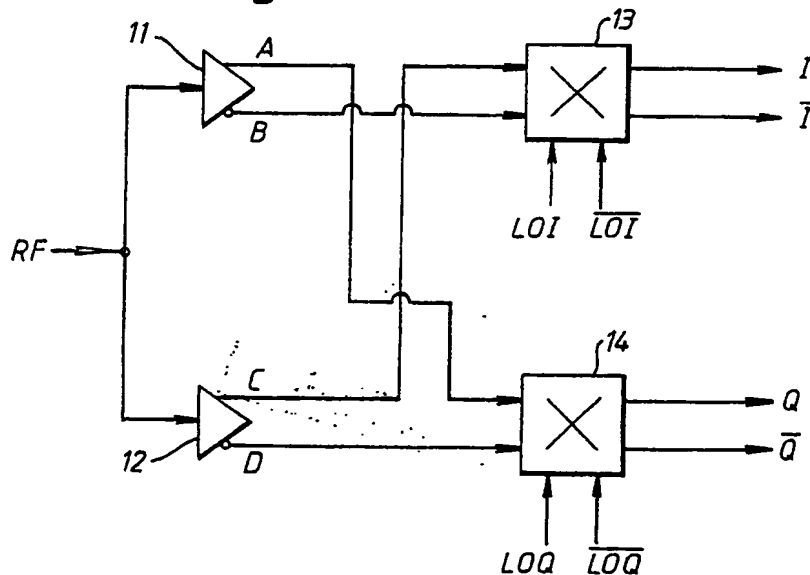
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(54) Cross-coupled mixer stage for zero IF radio.

(57) An arrangement for mixing r.f. signals with local oscillator signals wherein r.f. signals are fed to two separate balanced amplifiers the outputs of which are fed to two separate balanced mixer stages to-

gether with balanced local oscillator signals in quadrature, wherein the two outputs from each amplifier are fed one to each mixer stage.

Fig. 2.



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CROSS-COUPLED MIXER STAGE FOR ZERO IF RADIO

This invention relates to an arrangement for mixing r.f. signals with local oscillator signals in a zero IF or 'direct conversion' radio.

In one form of zero IF radio receiver local oscillator signals in phase quadrature at the carrier frequency are each separately mixed with the incoming modulated r.f. signal. The mixer outputs at the baseband are then low pass filtered and amplified before further signal processing is effected to achieve the required demodulation.

In a known zero IF radio receiver the incoming r.f. signals are fed to separate, balanced amplifiers the outputs of which are then fed to respective balanced mixer stages together with the respective quadrature local oscillator signals. The overall arrangement is shown in Fig. 1.

According to the present invention there is provided an arrangement for mixing r.f. signals with local oscillator signals wherein r.f. signals are fed to two separate balanced amplifiers with balanced outputs, which outputs are fed to two separate balanced mixer stages together with local oscillator signals in quadrature, characterised in that the two outputs from each amplifier are fed one to each mixer stage.

In a preferred embodiment of the invention the inphase and quadrature oscillator signals are balanced signals and the mixers are of the type commonly known as double balanced mixers.

An embodiment of the invention will now be described with reference to the accompanying drawings, in which:-

Fig. 1 is a block schematic of a prior art zero I.F. radio receiver mixer arrangement,

Fig. 2 is a block schematic of a zero I.F. radio receiver mixer arrangement according to the present invention, and

Fig. 3 is a circuit diagram of an embodiment of the arrangement of Fig. 2.

In the arrangement of Fig. 2 the incoming r.f. signals are fed to two separate balanced amplifiers 11, 12 which provide balanced outputs A, B and C, D. These balanced outputs are applied to two balanced mixer stages 13, 14 together with local oscillator signals LOI, LOI, LOQ and LOQ. LOQ and LOQ are in quadrature with LOI and LOI.

However, in contrast with the prior art, wherein the two outputs A & B of amplifier 11 are both fed to mixer 13, likewise C & D are fed to mixer 14, in the present invention the two outputs A & B from amplifier 11 are fed one to each mixer 13, 14, likewise the two outputs C, D from amplifier 12 are fed one to each mixer 13, 14.

The arrangement of Fig. 2 provides a reduction in the errors resulting from any mismatch differ-

ences in nominally identical components of the amplifying stages. It also provides a reduction in the output noise from each of the doubly balanced mixer stages. These improvements resulting from the reduced correlations between signals B and C, and between signals A and D, as compared with the correlations between signals A and B or between signals C and D in the prior art arrangement of Fig. 1.

Fig. 3 shows a circuit arrangement wherein the balanced amplifiers and mixers may be incorporated in a single integrated circuit. The incoming r.f. signals are applied to two balanced amplifiers formed by pairs of transistors T1, T2 and T3, T4 respectively. As shown the r.f. inputs are balanced, however one side may be decoupled if desired. The balanced outputs A, B and C, D of the amplifiers are fed to two balanced mixers formed by respective sets of transistors T5 - T8 and T9 - T12, where they are mixed with balanced local oscillator signals LOI, LOI and LOQ, LOQ in quadrature. The outputs of the mixer stages are balanced baseband signals I, I and Q, Q. As explained with reference to Fig. 2, the two outputs A and B of one amplifier are fed one to each balanced mixer, likewise the outputs C & D are fed one to each mixer, i.e. outputs A and C are "cross-connected".

To improve isolation in the inputs to the mixer stages and optional cascode amplifier stage, provided by transistors T13-T16, may be included between the amplifiers and the mixers. This may be advantageous if separate r.f. input pre-amplifier stage is not included.

Claims

1. An arrangement for mixing r.f. signals with local oscillator signals wherein r.f. signals are fed to two separate balanced amplifiers with balanced outputs, which outputs are fed to two separate balanced mixer stages together with local oscillator signals in quadrature, characterised in that the two outputs from each amplifier are fed one to each mixer stage.
2. An arrangement according to claim 1 characterised in that the quadrature oscillator signals are each balanced, and the mixers are both double balanced mixers.
3. An arrangement according to claim 2, characterised in that each of the differential amplifiers is a long-tailed pair transistor amplifier, and each of the double balanced mixers comprises two pairs of transistors with emitters connected

in common, the current to each such pair of emitters being supplied from the collector of one of the transistors of a long-tailed pair.

4. An arrangement according to claim 3 including a cascode amplifying stage between the amplifier output connections and the mixer inputs.

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Fig. 1. PRIOR ART.

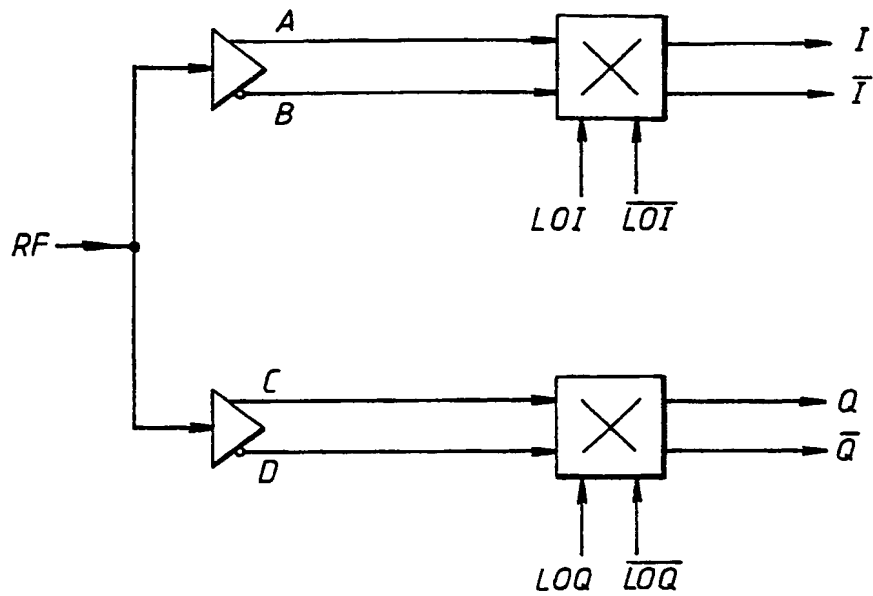


Fig. 2.

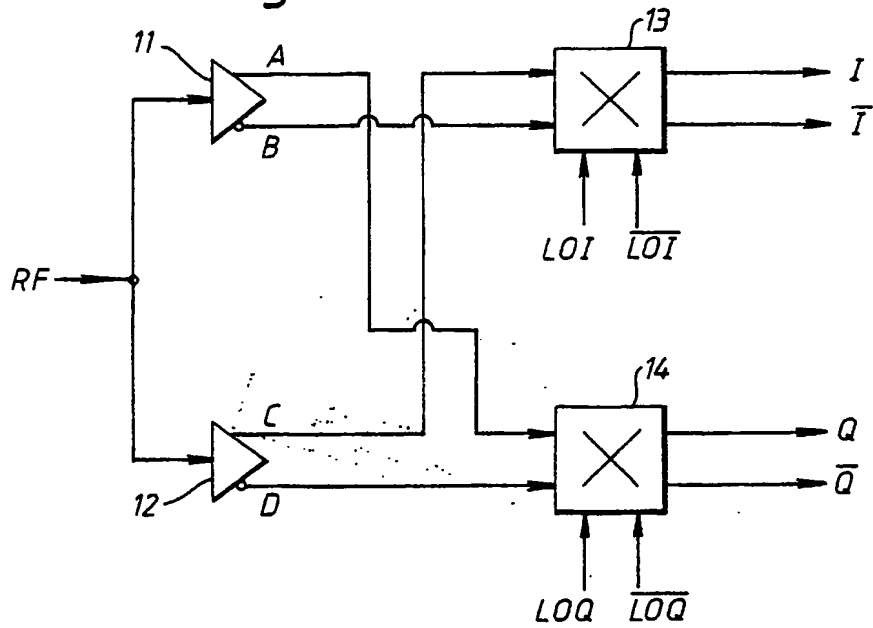
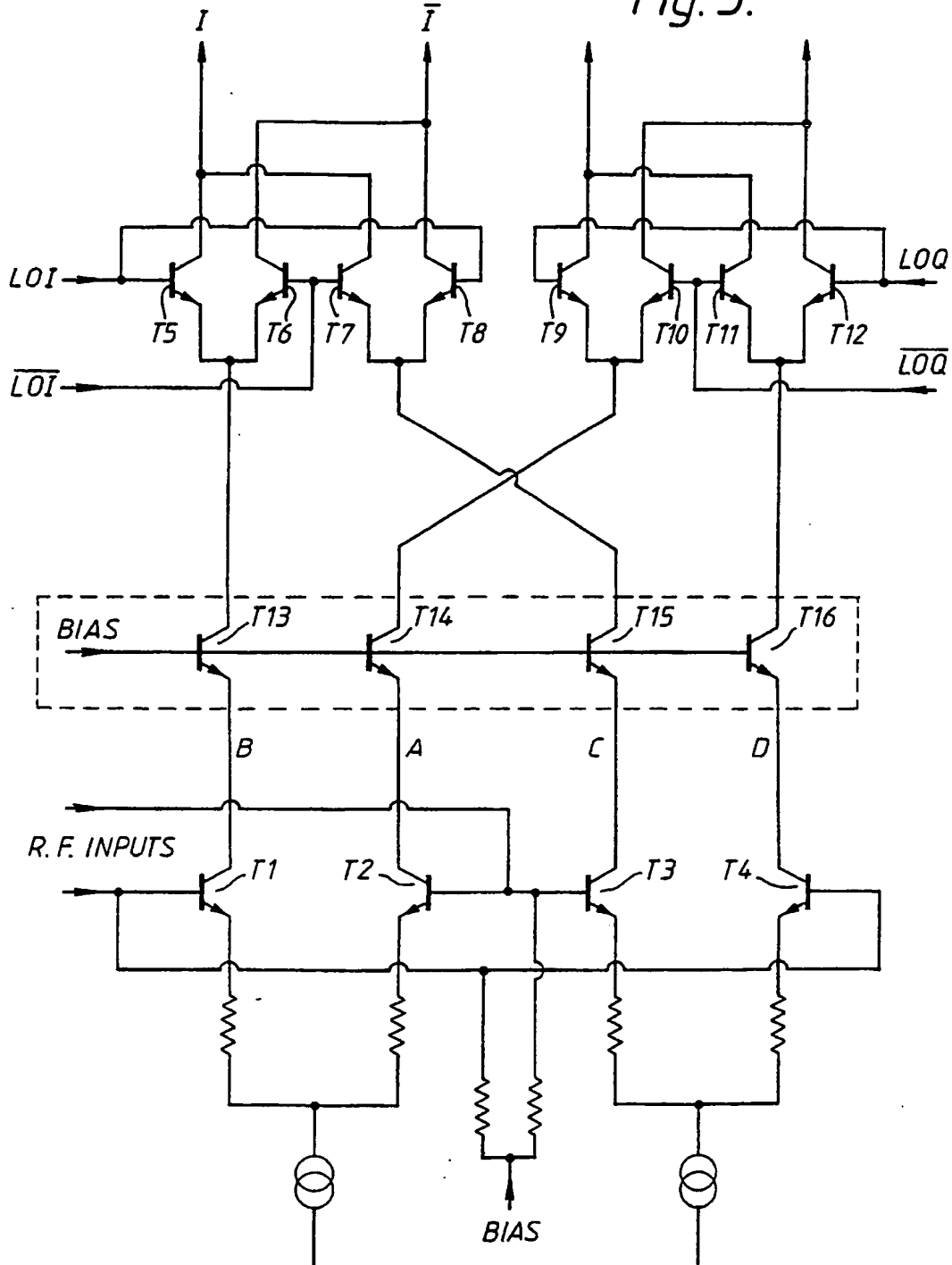


Fig. 3.





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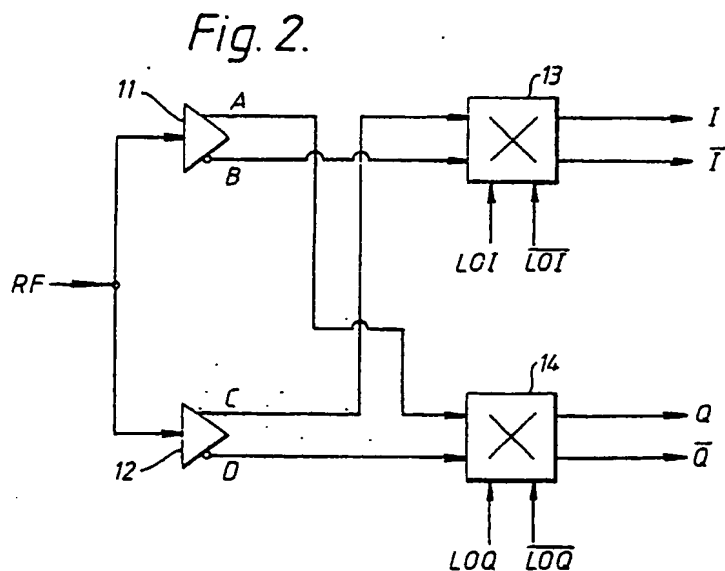
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54 Cross-coupled mixer stage for zero IF radio.

57 An arrangement for mixing r.f. signals with local oscillator signals wherein r.f. signals are fed to two separate balanced amplifiers (11,12) the outputs of which are fed to two separate balanced mixer stages

(13,14) together with balanced local oscillator signals in quadrature, wherein the two outputs from each amplifier are fed one to each mixer stage.



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EUROPEAN SEARCH REPORT

Application Number

EP 90 31 2306

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
Y	GB-A-2 177 860 (MULTITONE ELECTRONICS PLC) "page 1, line 35 - page 2, line 39; figures 3,5" - - -	1-3	H 03 D 7/14
Y	GB-A-2 151 863 (KABUSHIKI KAISHA TOSHIBA) "page 1, line 34 - page 4, line 13; figure 2" - - - - -	1-3	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H 03 D
The present search report has been drawn up for all claims			
Place of search		Date of completion of search	Examiner
The Hague		09 August 91	DHONDT I.E.E.
<div>CATEGORY OF CITED DOCUMENTS</div> <div>X: particularly relevant if taken alone</div> <div>Y: particularly relevant if combined with another document of the same category</div> <div>A: technological background</div> <div>O: non-written disclosure</div> <div>P: intermediate document</div> <div>T: theory or principle underlying the invention</div> <div>E: earlier patent document, but published on, or after the filing date</div> <div>D: document cited in the application</div> <div>L: document cited for other reasons</div> <div>&: member of the same patent family, corresponding document</div>			